

Ankit Prajapati

Academic governance & institutional AI

Senior Academic Administrator, Karnavati University | January 2025 – present

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[Professional profile on LinkedIn](#)

I build the documentation, digital systems and review processes that let a university report on itself with confidence — and I'm applying that same governance discipline to how institutions adopt AI.

A decade across engineering, teaching, state-government digital operations and higher-education governance. Recently: coordinated Karnavati University's first-ever NAAC Self-Study Report to an A+ result on the first attempt.

Direction

Seeking a founding institutional-AI leadership mandate in higher education. The proposed CAIO role builds on academic governance, digital-systems experience and demonstrated AI-assisted work. It is a target role, not a current designation.

[CAIO proposal and transformation map](#)



Engineering, teaching and governance

Senior Academic Administrator

Jan 2025 — Present | Karnavati University · Gandhinagar

Coordinated the university's first-ever Self-Study Report for the National Assessment and Accreditation Council (NAAC) as Internal Quality Assurance Cell (IQAC) Coordinator — A+ on the first attempt.

National Education Policy (NEP) 2020 Coordinator and single point of contact for the Knowledge Consortium of Gujarat (KCG); primary interface with the Directorate of Technical Education (DTE) and Government of Gujarat (GoG) across 30+ compliance cycles.

Leads ERP, Academic Bank of Credits and biometric-attendance rollouts across university student, faculty and staff workflows.

IT Establishing Engineer

Jun 2023 — Jan 2025 | Gujarat State Civil Supplies Corporation Ltd. · Gandhinagar

Designed a daily statistical reporting system tracking real-time food-stock movement across 260+ godowns statewide.

Co-led the Command & Control Centre initiative, reporting directly to the corporation's Indian Administrative Service (IAS) Managing Director & Chairman; sole Website Nodal Officer.

Ran Farmer Procurement Portal operations for 50,000+ farmers per season.

Assistant Professor & Academic Administrator

Jul 2016 — Mar 2023 | Shankersinh Vaghela Bapu Institute of Technology · Gandhinagar

Seven-year tenure combining teaching with institute-wide Gujarat Technological University (GTU) examination coordination.

Nodal Officer for the Admission Committee for Professional Courses (ACPC) for two years; coordinated industry partnerships, placements and student mentoring.

Assistant Professor & Academic Administrative Officer

Apr 2015 — Jul 2016 | Babaria Institute of Technology · Vadodara

Taught and administered academic operations; introduced a LaTeX-based technical review-paper requirement for undergraduates.

Academic Associate · Teaching & Research Support

Jul 2014 — Mar 2015 | S.V. National Institute of Technology (SVNIT) · Surat

Delivered UG coursework in Electromagnetic Wave & Radiating Systems; contributed to an IEEE conference and a research publication.

Validation Software Engineer (M.Tech Training)

Jul 2013 — Jun 2014 | STMicroelectronics India · Greater Noida

Validated set-top-box embedded software — test-case development, debugging and workflow automation in Linux.

An institutional systems perspective

Institutional governance & accreditation

NAAC / IQAC accreditation, Self-Study Report authorship, NEP 2020 alignment and evidence management. Organising institutional requirements into clear ownership, usable templates and review processes.

Digital academic systems

ERP deployment, Academic Bank of Credits, biometric attendance and MIS reporting across university academic operations. Translating operational needs into workflows, ownership and maintainable structure.

Responsible AI in institutions

AI-assisted governance systems designed around provenance, accountable review and human authority — building the practical guardrails a university needs before it scales AI into decisions.

Engineering & applied research

An electronics & communication engineering foundation with part-time doctoral research in low-power RISC-V hardware for embedded and edge computing — systems thinking, testing and technical documentation.

Education

Ph.D. (Part-Time), Electronics & Communication Engineering — in progress

Karnavati School of Research · Supervisor: Dr. Lakshmanan M. · Low-power RISC-V hardware for embedded / edge computing.

M.Tech, Communication Systems · 2014

Nirma University, Ahmedabad.

B.E., Electronics & Communication Engineering · 2011

Ahmedabad Institute of Technology, Gujarat University.

Research and credentials

Doctoral research

Ph.D. (Part-Time) in ECE at Karnavati School of Research (in progress), supervised by Dr. Lakshmanan M. — low-power RISC-V hardware architecture for intelligent embedded and edge-computing systems.

Patent

A patent application filed in biomedical engineering (under examination), with a second in preparation.

Publications

Peer-reviewed research publications in the medical field, plus an IEEE conference contribution (SVNIT) and a technical publication from the STMicroelectronics placement.

Awards & distinction

Gujarat Public Service Commission (GPSC), Gujarat Administrative Service (GAS) Class 1 — cleared the preliminary examination in the top 8 of 1,00,000+ registered candidates; appeared for the mains descriptive examination.

Technical foundation

Python C Linux Bash Git RISC-V / VLSI Embedded systems LaTeX MATLAB VHDL

Professional development

Google for Education (Higher Education) · Outcome-Based Education · NAAC Accreditation Systems · NEP Implementation.

Practical demonstrations. Visible decisions.

Naming what a return can prove

Working prototype

Continuous quality assurance for NAAC accreditation — keeping real evidence, mid-year estimate and refused prediction visibly distinct.

[Explore the work](#)

Governing the NEP return

Working prototype

Turning an ambiguous, university-wide reporting obligation into one clear chain of accountability — ownership, approval and correction history by design.

[Explore the work](#)

Evidence with a history

Implementation baseline

Designing provenance, permissions and accountable review for an institutional assurance archive — before any of it is switched on.

[Explore the work](#)

Making a timetable work

Synthetic demonstration

Reducing a broad ERP ambition to one solvable problem: a constraint-based scheduling engine with independent validation.

[Explore the work](#)

Operating principles

Assign responsibility before collecting data. Preserve evidence and correction history. Keep human review visible. Design systems that an institution can maintain after the first author moves on.

The creations use demonstration or de-identified data and browser-local state. Individual dossiers distinguish prototypes, implementation baselines and synthetic demonstrations.

[Explore the full career profile](#)